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1 [Visualization challenges for a new cyberpharmaceutical computing paradigm](#)

Russell J. Turner, Kahr Chaturvedi, Nathan J. Edwards, Daniel Fasulo, Aaron L. Halpern, Daniel H. Huxon, Oliver Kohlbacher, Jason R. Miller, Knut Reinert, Karin A. Remington, Russell Schwartz, Brian Walenz, Shibo Yoseph, Sorin Istrail

October 2001 **PVG '01: Proceedings of the IEEE 2001 symposium on parallel and large-data visualization and graphics**

Publisher: IEEE Press

Full text available: (3.07 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

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In recent years, an explosion in data has been profoundly changing the field of biology and creating the need for new areas of expertise, particularly in the handling of data. One vital area that has so far received insufficient attention is how to communicate ...

2 [The future of our profession](#)

Bo Dahlbom, Lars Mathiassen

June 1997 **Communications of the ACM**, Volume 40 Issue 6

Publisher: ACM

Full text available: (432.38 KB) Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

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3 [Predictive modeling in automotive direct marketing: tools, experiences and open issues](#)

Wendy Gersten, Rüdiger Wirth, Dirk Arndt

August 2000 **KDD '00: Proceedings of the sixth ACM SIGKDD international conference on Knowledge discovery and data mining**

Publisher: ACM

Full text available: (334.76 KB) Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 91, Downloads (Overall): 936, Citation Count: 5

Keywords: CRISP-DM, Clementine, data mining process, direct marketing, evaluation measures

4 [Modeling software design diversity: a review](#)

Rev Littlewood, Peter Popov, Lorenzo Strigini

June 2001 **Computing Surveys (CSUR)**, Volume 33 Issue 2

Publisher: ACM

Full text available: (259.57 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

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14 [Understanding the backward slices of performance degrading instructions](#)



[Craig B. Zilles, Gurindar S. Sohi](#)

June **ISCA '00: Proceedings of the 27th annual international symposium on Computer architecture**
2000

Publisher: ACM

Full text available: [PDF](#) (128.47 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

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For many applications, branch mispredictions and cache misses limit a processor's performance to a level well below its peak instruction throughput. A small fraction of static instructions, whose behavior cannot be anticipated using current branch ...

Also published in:

May 2000 **SIGARCH Computer Architecture News** Volume 28 Issue 2

15 [Designing data marts for data warehouses](#)



October **Transactions on Software Engineering and Methodology (TOSEM)**, Volume 10 Issue 4
2001

Publisher: ACM [Request Permissions](#)

Full text available: [PDF](#) (203.43 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

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Data warehouses are databases devoted to analytical processing. They are used to support decision-making activities in most modern business settings, when complex data sets have to be studied and analyzed. The technology for analytical processing assumes ...

Keywords: conceptual modeling, data mart, data warehouse, design method, software quality management

16 [Smart packets: applying active networks to network management](#)



[Beverly Schwartz, Alden W. Jackson, W. Timothy Strayer, Wenyi Zhou, P. Dennis Rockwell, Craig Patridge](#)

February **Transactions on Computer Systems (TOCS)**, Volume 18 Issue 1
2000

Publisher: ACM [Request Permissions](#)

Full text available: [PDF](#) (190.33 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 57, Downloads (Overall): 830, Citation Count: 13

This article introduces Smart Packets and describes the smart Packets architecture, the packet formats, the language and its design goals, and security considerations. Smart Packets is an Active Networks project focusing on applying active networks technology ...

Keywords: active networks

17 [Digital library technology for locating and accessing scientific data](#)



[Robert E. McGrath, Joe Fufreile, Ray Ranta, Damien Guillaume](#)

August 1999 **DL '99: Proceedings of the fourth ACM conference on Digital libraries**

Publisher: ACM [Request Permissions](#)

Full text available: [PDF](#) (83.37 KB)

Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 10, Downloads (12 Months): 42, Downloads (Overall): 597, Citation Count: 2

Keywords: astronomy data, information retrieval, scientific data, scientific information systems

18 [Multi-processor performance on the Tera MTA](#)

[Allan Savely](#), [Larry Carter](#), [Jay Boisseau](#), [Amit Majumdar](#), [Kang Su Gatin](#), [Nick Mitchell](#), [John Feo](#), [Brian Koblenz](#)

November **Supercomputing '98: Proceedings of the 1998 ACM/IEEE conference on Supercomputing 1998** (CDROM)

Publisher: IEEE Computer Society

Full text available:  [PDF](#) (36.39 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 12, Downloads (Overall): 141, Citation Count: 11

The Tera MTA is a revolutionary commercial computer based on a multithreaded processor architecture. In contrast to many other parallel architectures, the Tera MTA can effectively use high amounts of parallelism on a single processor. By running multiple ...


Keywords: NAS parallel benchmarks, parallelizing compilers, performance evaluation, performance tuning, scalability analysis, tera multithreaded architecture

19 [From VHDL to efficient and first-time-right designs: a formal approach](#)

 [Peter F. A. Middelboek](#), [Sreenanga P. Rajan](#)

April **Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 1 Issue 2 1996

Publisher: ACM  [PDF](#) (722.99 KB)

Full text available:  [PDF](#) (722.99 KB)

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Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 70, Downloads (Overall): 611, Citation Count: 2

In this article we provide a practical transformational approach to the synthesis of correct synchronous digital hardware designs from high-level specifications. We do this while taking into account the complete life cycle of a design from early prototype ...


Keywords: CDFG, SFG, VHDL, correctness by construction, design methodology, rapid system prototyping, transformational design

20 [Delivering presentations from multimedia servers](#)

[Nevzat Hurkan Balkir](#), [Gultekin Ozyozulu](#)

December **The VLDB Journal — The International Journal on Very Large Data Bases**, Volume 7 Issue 4

Publisher: Springer-Verlag New York, Inc.

Full text available:  [PDF](#) (171.48 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 41, Downloads (Overall): 342, Citation Count: 4

Most multimedia servers reported in the literature are designed to serve multiple and independent video/audio streams. We think that, in future, multimedia servers will also serve complete presentations. Multimedia presentations provide unique opportunities ...





Keywords: Admission control, Buffer management, Flattening, Multimedia presentations

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